| Form 1449 (Modified) | | Atty Docket No. CISCP100 | | U.S.S.N. Unknown |
|---|---|---|---|---|
| **Information Disclosure Statement By Applicant** | | Applicant: Kartik Chandran, *et al.* | | |
| (Use Several Sheets if Necessary) | | Filing Date Herewith | | Group Unknown |

## U.S. Patent Documents

| Examiner Initial | No. | Patent No. | Date | Patentee | Class | Sub-Class | Filing Date |
|---|---|---|---|---|---|---|---|
| ᗞN | A | 5,838,915 | 11/17/98 | Klausmeier, et al. | 395 | 200.45 | 11/17/97 |
| | B | | | | | | |
| | C | | | | | | |
| | D | | | | | | |
| | E | | | | | | |
| | F | | | | | | |
| | G | | | | | | |
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| | I | | | | | | |
| | J | | | | | | |
| | K | | | | | | |

## Foreign Patent or Published Foreign Patent Application

| Examiner Initial | No. | Document No. | Publication Date | Country or Patent Office | Class | Sub-class | Translation Yes | No |
|---|---|---|---|---|---|---|---|---|
| | L | | | | | | | |
| | M | | | | | | | |
| | N | | | | | | | |
| | O | | | | | | | |
| | P | | | | | | | |

## Other Documents

| Examiner Initial | No. | Author, Title, Date, Place (e.g. Journal) of Publication |
|---|---|---|
| ᗞN | 1R | R. Ladner, "CSE 326: Data Structures", Spring 1998, Lecture Overheads: Lecture 19, Calendarqueues. (Month Unknown) 04/10/08 \Dⁱ\ |
| ᗞN | 1S | Randy Brown, "Calendar Queues: A Fast 0(1) Priority Queue Implementation For the Simulation Even Set Problem", October 1988, Volume 31, Communications of the ACM. |
| ᗞN | 1T | LSI Logic Corporation, "LSI Logic's L64364 ATMizer II+ ATM-SAR Chip Technical Manual" Chapter 7, November 16, 1998. |

| Examiner | | Date Considered |
|---|---|---|
| *[signature]* | | 05/07/02 |

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.